CLAIMS

What is claimed is:

1.   A system comprising:

a peripheral device adapted to define a plurality of device functions accessible through a data interface with a data bus;

a first processing system adapted to communicate with a first device function of the peripheral device through the data interface; and

a second processing system adapted to communicate with a second function of the peripheral device through the data interface.

2.   The system of claim 1, wherein the first processing system comprises logic to enumerate each device function associated with an I/O channel.

3.   The system of claim 2, wherein the first device function comprises logic to communicate with a RAID channel.

4.   The system of claim 1, wherein the second processing system is coupled to the data bus through a bridge and the first processing system is a peripheral device

5.   The system of claim 1, wherein the first processing system comprises logic to cause the peripheral device to conceal one or more device functions from the second processing system.

6.   The system of claim 5, wherein the first processing system comprises:

logic to enumerate a first device function of the peripheral device; and

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logic to set information in a configuration header maintained at the peripheral device to conceal the first function from the second processing system.

7.   The system of claim 5, wherein the system further comprises a bridge coupled to the peripheral device through a secondary bus, and wherein the bridge comprises logic to initiate execution of an enumeration process by the first processing system prior to completion of an enumeration process by the second processing system.

8.   The system of claim 5, wherein the first processing system comprises logic to transmit a signal to the peripheral device to inhibit enumeration of the peripheral device by the second processing system.

9.   A method comprising:

initiating a first enumeration procedure at a first processing system to enumerate a first device function of a peripheral device coupled to a data interface of a data bus; and

initiating a second enumeration procedure at a second processing system to enumerate a second device function of the peripheral device.

10.   The method of claim 9, the method further comprising enumerating at least one device function associated with an I/O channel.

11.   The method of claim 10, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

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12.   The method of claim 9, wherein the second processing system is coupled to the data bus through a bridge and the first processing system is a peripheral device

13.   The method of claim 9, the method further comprising causing the peripheral device to conceal one or more device functions from the second processing system.

14.   The method of claim 13, wherein the method further comprises:

enumerating a first device function of the peripheral device; and

setting information in a configuration header maintained at the peripheral device to conceal the first function from the second processing system.

15.   The method of claim 13, wherein the method further comprises initiating execution of an enumeration process by the first processing system prior to completion of an enumeration process by the second processing system.

16.   The method of claim 13, wherein the method further comprises transmitting a signal to the peripheral device to inhibit enumeration of the peripheral device by the second processing system.

17.   An article comprising:

storage medium comprising machine-readable instructions stored thereon for:

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initiating a first enumeration procedure at a first processing system to enumerate

a first device function of a peripheral device coupled to a data interface of a data bus;

and

initiating a bus transaction on the data bus to cause the first device function to

be concealed from subsequent enumeration procedures.

18.   The article of claim 17, wherein the storage medium further comprises

machine-readable instructions stored thereon for enumerating the first device function

as an I/O channel.

19.   The article of claim 18, wherein the device function associated with the

I/O channel comprises logic to communicate with a RAID channel.

20.   The article of claim 17, wherein the storage medium further comprises

machine-readable instructions stored thereon for initiating a bus transaction to set

information in a configuration header maintained at the peripheral device to conceal the

first device function from subsequent enumeration procedures.

21.   A processing system comprising:

logic to initiate a first enumeration procedure to enumerate a first device

function of a peripheral device coupled to a data interface of a data bus; and

logic to initiate a bus transaction on the data bus to cause the first device

function to be concealed from subsequent enumeration procedures.

22. The processing system of claim 21, the processing system further comprising logic to enumerate the first device function as an I/O channel.

23. The processing system of claim 22, wherein the device function associated with the I/O channel comprises logic to communicate with a RAID channel.

24. The processing system of claim 17, wherein the processing system further comprises logic to initiate a bus transaction to set information in a configuration header maintained at the peripheral device to conceal the first device function from subsequent enumeration procedures.

25. The processing system of claim 24, wherein the data bus comprises a PCI data bus and the processing system further comprises logic to initiate a bus transaction to modify data in a Header Type register of the configuration header.